Facultyof Engineering & Technology

Program : Master of Technology (Electronics & Communication Engineering) : 2022-23 onwards

														Theory		Theory (Internal)					Practical				Practical (Internal)						
Semester/Year	Council Code (If any)	Subject Code (If already existing)	Nomenclature	Theory/ Practical/	Core/ AECC/ SEC/ VAC/ DSE/ GE	For ERP	Theory/Practical (Result Combined or Separate)	L	T	P	Credits	Contact Hours	Max	Pass	Mid Term	Assignment	Project/Seminar/Activity	Max	Pass	Demonstration/Presentation	Viva-voce	Max	Pass	Attendance	Viva-Voce in each lab class	Lab Work	Evaluation of Performance of Expe	Max	Pass	Overall Pass Marks	Scheme of Examinations [Theory+Internal or Practical+Internal]
	N/A	13080101	Advanced Microprocessor & Microcontrollers	Theory	Core	Core	Separate	3	1	0	4	4	60	24	20	10	10	40	16											40	Theory + Internal
	N/A	13080143	Modern Information & Communication Theory	Theory	Core	Core	Separate	3	1	0	4	4	60	24	20	10	10	40	16											40	Theory + Internal
	N/A	13080144	Advanced Microprocessor & Microcontrollers Lab	Practical	Core	Core	Separate	0	0	2	1	2								20	20	40	16	10	10	10	30	60	24	40	Practical+Internal
			Choose any one from below list of subject in Program elective - I																												
	N/A	13080106	Digital VLSI	Theory	DSE	DSE	Separate	3	1	0	4	4	60	24	20	10	10	40	16											40	Theory + Internal
I	N/A	13080110	Computer Communication and Networks	Theory	DSE	DSE	Separate	3	1	0	4	4	60	24	20	10	10	40	16											40	Theory + Internal
	N/A	13080145	Digital Logic Design	Theory	DSE	DSE	Separate	3	1	0	4	4	60	24	20	10	10	40	16											40	Theory + Internal
			Choose any one from below list of Labs in Program elective - I Labs																												
	N/A	13080146	Digital VLSI Lab	Practical	DSE	DSE	Separate	0	0	2	1	2								20	20	40	16	10	10	10	30	60	24	40	Practical+Internal
	N/A	13080147	Computer Communication and Networks Lab	Practical	DSE	DSE	Separate	0	0	2	1	2								20	20	40	16	10	10	10	30	60	24	40	Practical+Internal
	N/A	13080148	Digital Logic Design Lab	Practical	DSE	DSE	Separate	0	0	2	1	2								20	20	40	16	10	10	10	30	60	24	40	Practical+Internal
			Total for the I Semester					**9**	**3**	**4**	**14**	**16**																			
	N/A	13080250	ARM Processor	Theory	Core	Core	Separate	3	1	0	4	4	60	24	20	10	10	40	16											40	Theory + Internal
	N/A	13080251	Mixed Signal Design	Theory	Core	Core	Separate	3	1	0	4	4	60	24	20	10	10	40	16											40	Theory + Internal
	N/A	13080252	ARM Processor Lab	Practical	Core	Core	Separate	0	0	2	1	2								20	20	40	16	10	10	10	30	60	24	40	Practical+Internal
	N/A	13080253	Problem Identification	Practical	Core	Core	Separate	0	0	2	1	2								20	20	40	16	10	10	10	30	60	24	40	Practical+Internal
			Choose any one from below list of subject in Program elective - II																												
II	N/A	13080254	Analog VLSI	Theory	DSE	DSE	Separate	3	1	0	4	4	60	24	20	10	10	40	16											40	Theory + Internal
	N/A	13080255	Microwave Theory & Circuits	Theory	DSE	DSE	Separate	3	1	0	4	4	60	24	20	10	10	40	16											40	Theory + Internal
	N/A	13080256	ASIC Design	Theory	DSE	DSE	Separate	3	1	0	4	4	60	24	20	10	10	40	16											40	Theory + Internal
			Choose any one from below list of Labs in Program elective - II Labs																												
	N/A	13080257	Analog VLSI Lab	Theory	DSE	DSE	Separate	0	0	2	1	2								20	20	40	16	10	10	10	30	60	24	40	Practical+Internal
	N/A	13080258	Microwave Theory & Circuits Lab	Theory	DSE	DSE	Separate	0	0	2	1	2								20	20	40	16	10	10	10	30	60	24	40	Practical+Internal
	N/A	13080259	ASIC Design Lab	Theory	DSE	DSE	Separate	0	0	2	1	2								20	20	40	16	10	10	10	30	60	24	40	Practical+Internal
			Total for IInd Semester					**9**	**3**	**6**	**15**	**18**																			
	N/A	13080319	Industrial Internship	Practical	Core	Core	Separate	0	0	2	1	2								20	20	40	16	10	10	10	30	60	24	40	Practical+Internal
	N/A	13080318	Dissertation Phase - I	Practical	Core	Core	Separate	0	0	10	5	10								20	20	40	16	10	10	10	30	60	24	40	Practical+Internal
			Choose any one from below list of subject in Program elective - III																												
	N/A	13080320	Memory Design and Testing	Theory	DSE	DSE	Separate	3	1	0	4	4	60	24	20	10	10	40	16											40	Theory + Internal
	N/A	13080321	Communication Hardware	Theory	DSE	DSE	Separate	3	1	0	4	4	60	24	20	10	10	40	16											40	Theory + Internal
	N/A	13080322	Image and Video Processing	Theory	DSE	DSE	Separate	3	1	0	4	4	60	24	20	10	10	40	16											40	Theory + Internal
			Choose any one from below list of subject in Program elective - IV																												
	N/A	13080323	Introduction to Nanoelectronics	Theory	DSE	DSE	Separate	3	1	0	4	4	60	24	20	10	10	40	16											40	Theory + Internal
	N/A	13080324	Detection & Estimation Theory	Theory	DSE	DSE	Separate	3	1	0	4	4	60	24	20	10	10	40	16											40	Theory + Internal
III	N/A	13080104	Advanced Digital Signal Processing	Theory	DSE	DSE	Separate	3	1	0	4	4	60	24	20	10	10	40	16											40	Theory + Internal
			Choose any one from below list of Labs in Program elective - III Labs																												

Facultyof Engineering & Technology

Program : Master of Technology (Electronics & Communication Engineering) : 2022-23 onwards

														Theory		Theory (Internal)					Practical				Practical (Internal)							
Semester/Year	Council Code (If any)	Subject Code (If already existing)	Nomenclature	Theory/ Practical/	Core/ AECC/ SEC/ VAC/ DSE/ GE	For ERP	Theory/Practical (Result Combined or Separate)	L	T	P	Credits	Contact Hours	Max	Pass	Mid Term	Assignment	Project/Seminar/Activity	Max	Pass	Demonstration/Presentation	Viva-voce	Max	Pass	Attendance	Viva-Voce in each lab class	Lab Work	Evaluation of Performance of Expe	Max	Pass	Overall Pass Marks	Scheme of Examinations [Theory+Internal or Practical+Internal]
	N/A	13080325	Memory Design and Testing Lab	Practical	DSE	DSE	Separate	0	0	2	1	2								20	20	40	16	10	10	10	30	60	24	40	Practical+Internal
	N/A	13080326	Communication Hardware Lab	Practical	DSE	DSE	Separate	0	0	2	1	2								20	20	40	16	10	10	10	30	60	24	40	Practical+Internal
	N/A	13080327	Image and Video Processing Lab	Practical	DSE	DSE	Separate	0	0	2	1	2								20	20	40	16	10	10	10	30	60	24	40	Practical+Internal
			Choose any one from below list of Labs in Program elective - IV Labs																												
	N/A	13080328	Introduction to Nanoelectronics Lab	Practical	DSE	DSE	Separate	0	0	2	1	2								20	20	40	16	10	10	10	30	60	24	40	Practical+Internal
	N/A	13080329	Detection & Estimation Theory Lab	Practical	DSE	DSE	Separate	0	0	2	1	2								20	20	40	16	10	10	10	30	60	24	40	Practical+Internal
	N/A	13080330	Advanced Digital Signal Processing Lab	Practical	DSE	DSE	Separate	0	0	2	1	2								20	20	40	16	10	10	10	30	60	24	40	Practical+Internal
			Total for IInd Semester					**6**	**2**	**16**	**16**	**24**																			
IV	N/A	13080402	Dissertation Phase - II	Practical	Core	Core	Separate	0	0	30	15	30	-	-	-	-	-	-	-	75	75	150	60	40	45	40	125	250	100	160	Practical+Internal
			Total for IVth Semester					**0**	**0**	**30**	**15**	**30**																			
			Total Credits for Degree								**60**																				